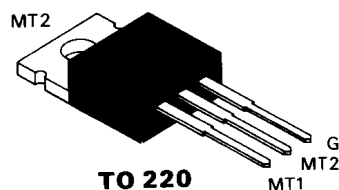


**T0805BH –  
T0805NH TRIACS**
**8.0 A 200–800 V  
5/5/5/5 mA**

The T0805 series of TRIAC's are high performance glass passivated PNP devices. These parts are intended for general purpose applications where logic compatible gate sensitivity is required.


**TO 220**
**Absolute Maximum Ratings**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Parameter	Part Nr.	Symbol	Min.	Max.	Unit	Test Conditions
Repetitive Peak Off State Voltage	<b>T0805BH</b> <b>T0805DH</b> <b>T0805MH</b> <b>T0805NH</b>	$V_{DRM}$	200 400 600 800		V	$T_j = -40^\circ\text{C}$ to $125^\circ\text{C}$ $R_{GK} = 1\text{K}\Omega$
On- State Current		$I_{T(RMS)}$	8		A	
Nonrept. On- State Current		$I_{TSM}$	77		A	
Nonrept. On- State Current		$I_{TSM}$	70		A	
Fusing Current		$I^2t$	24		$\text{A}^2\text{s}$	$t = 10\text{ ms}$
Peak Gate Current		$I_{GM}$	4		A	10 $\mu\text{s}$ max.
Peak Gate Dissipation		$P_{GM}$	10		W	10 $\mu\text{s}$ max.
Gate Dissipation		$P_{G(AV)}$	1		W	20 ms max.
Operating Temperature		$T_j$	-40	125	$^\circ\text{C}$	
Storage Temperature		$T_{stg}$	-40	125	$^\circ\text{C}$	
Soldering Temperature		$T_{sld}$		250	$^\circ\text{C}$	1.6 mm from case, 10 s max.

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Min.	Max.	Unit	Test Conditions
Off- State Leakage Current	$I_{DRM}$		2	mA	$V_D = V_{DRM}$ $R_{GK} = 1\text{K}\Omega$ $T_j = 125^\circ\text{C}$
Off- State Leakage Current	$I_{DRM}$		5	$\mu\text{A}$	$V_D = V_{DRM}$ $R_{GK} = 1\text{K}\Omega$ $T_j = 25^\circ\text{C}$
On- State Voltage	$V_T$		1.85	V	at $I_T = 12\text{ A}$ , $T_j = 25^\circ\text{C}$
On- State Threshold Voltage	$V_{T(TO)}$		1.0	V	$T_j = 125^\circ\text{C}$
On- State Slope Resistance	$r_T$		80	m $\Omega$	$T_j = 125^\circ\text{C}$
Gate Trigger Current	$I_{GT\text{ I}+}$ (1)		5	mA	$V_D = 12\text{ V}$
	$I_{GT\text{ I}-}$ (2)		5	mA	$V_D = 12\text{ V}$
	$I_{GT\text{ III}-}$ (3)		5	mA	$V_D = 12\text{ V}$
	$I_{GT\text{ III}+}$ (4)		5	mA	$V_D = 12\text{ V}$
Gate Trigger Voltage	$V_{GT}$		2.5	V	$V_D = 12\text{ V}$ All Quadrants
Holding Current	$I_H$		5	mA	$R_{GK} = 1\text{K}\Omega$
Critical Rate of Voltage Rise	$dv/dt$	10		V/ $\mu\text{s}$	$V_D = .67 \times V_{DRM}$ $R_{GK} = 1\text{K}\Omega$ $T_j = 125^\circ\text{C}$
Critical Rate of Rise, Off- State	$dv/dt_c$	1		V/ $\mu\text{s}$	$I_T = 8\text{ A}$ $di/dt = 3.55\text{ A/ms}$ $T_C = 85^\circ\text{C}$
Thermal Resistance junc. to case	$R_{\theta jc}$		3	K/W	
Thermal Resistance junc. to amb.	$R_{\theta ja}$		60	K/W	

**T08**